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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/750,252	12/31/2003	Chang Hun Han	20063/OG03-047	8844
34431	7590	03/07/2005	EXAMINER	
HANLEY, FLIGHT & ZIMMERMAN, LLC 20 N. WACKER DRIVE SUITE 4220 CHICAGO, IL 60606			BOOTH, RICHARD A	
			ART UNIT	PAPER NUMBER
			2812	

DATE MAILED: 03/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

AK

Office Action Summary	Application No.	Applicant(s)	
	10/750,252	HAN, CHANG HUN	
	Examiner	Art Unit	
	Richard A. Booth	2812	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 13 December 2004.
 2a) This action is **FINAL**. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-4 and 6 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1-4 and 6 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|-------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>0304</u> . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 6 is rejected under 35 U.S.C. 102(b) as being anticipated by Peng, U.S. Patent 6,306,708.

Peng shows the invention as claimed including a method for fabricating a nonvolatile memory device comprising: forming a first floating gate 114; and forming a second floating gate 114 adjacent the first floating gate, the first and second floating gates being separated by less than a lithographic minimum feature size (see figs. 2E-2F and col. 3-lines 22-38).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Scott, U.S. Patent 6,627,524 in view of Pradeep et al., U.S. Patent 5,866,448 or Zhou et al., U.S. Patent 5,858,847.

Scott shows the invention substantially as claimed including a method for fabricating a nonvolatile memory device comprising: forming an isolation layer and a non-active region in a semiconductor substrate 12; forming an oxide layer 14 and a polysilicon layer 15 on the substrate; forming a sacrificial layer 16 on the polysilicon layer; forming polymer layers 24 on sidewalls of the sacrificial layer; and forming a floating gate and a tunnel oxide using the sacrificial layer and polymer layers as an etching mask (see figs. 1-5 and col. 2-line 56 to col. 4-line 67).

Scott is applied as above but fails to expressly disclose the polymer layers being formed by patterning the sacrificial layer, the polymer layers being generated from the etching of the sacrificial layer.

Pradeep et al. discloses patterning a sacrificial layer (40,42) comprising an oxide in order to form a polymer layer 44 on the sidewalls used to form a gate electrode (see figs. 3-4 and col. 4-lines 18-48). Alternatively, Zhou et al. discloses patterning a sacrificial layer (23,24) comprising TEOS oxide in order to form a polymer layer 26 on the sidewalls used to form a gate electrode (see figs. 1-4 and col. 3-line 66 to col. 5-line 53). In view of these disclosures, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the process of Scott so as to form the polymer layers by etching the sacrificial layer because this would reduce the overall number of process steps, thereby decreasing process time.

Concerning the particular spacing between adjacent polymer layers, it would have been obvious to one of ordinary skill in the art at the time the invention was made to determine through routine experimentation the desired spacing based upon various

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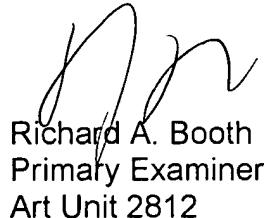
considerations such as the desired degree of integration and such limitation would not lend patentability to the instant application absent a showing of unexpected results.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Richard A. Booth whose telephone number is (571) 272-1668. The examiner can normally be reached on Monday-Thursday from 7:30-6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Lebentritt can be reached on (571) 272-1873. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Richard A. Booth
Primary Examiner
Art Unit 2812

February 28, 2005